

## TRANSMITTAL

Electronic Version v1.1

Stylesheet Version v1.1.0

Title of Invention	STRUCTURE AND METHOD OF MAKING A BIPOLAR TRANSISTOR HAVING REDUCED COLLECTOR-BASED CAPACITANCE							
<p>Application Number :</p> <p>Date :</p> <p>First Named Applicant:            Hiroyuki Akatsu</p> <p>Confirmation Number:</p> <p>Attorney Docket Number:        FIS920030415US1</p>								
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<table border="1"><thead><tr><th data-bbox="133 1010 773 1050">Submitted By:</th><th data-bbox="773 1010 1110 1050">Elec. Sign.</th><th data-bbox="1110 1010 1412 1050">Sign. Capacity</th></tr></thead><tbody><tr><td data-bbox="133 1050 773 1125">H. Daniel Schnurmann Registered Number: 35791</td><td data-bbox="773 1050 1110 1125">H. Daniel Schnurmann /s/</td><td data-bbox="1110 1050 1412 1125">Agent</td></tr></tbody></table>			Submitted By:	Elec. Sign.	Sign. Capacity	H. Daniel Schnurmann Registered Number: 35791	H. Daniel Schnurmann /s/	Agent
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Documents being submitted:	Files
us-ids	FIS920030415US1-usidst.xml us-ids.dtd us-ids.xsl
us-request	FIS920030415US1-usrequ.xml us-request.dtd us-request.xsl
us-assignment	FIS920030415US1-usassn.xml us-assignment.xsl us-assignment.dtd FIS920030415US1Assignment1.tif FIS920030415US1Assignment2.tif FIS920030415US1Assignment3.tif
us-fee-sheet	FIS920030415US1-usfees.xml us-fee-sheet.xsl us-fee-sheet.dtd
us-declaration	FIS920030415US1Declaration3.tif
us-declaration	FIS920030415US1Declaration4.tif
application-body	FIS920030415US1-trans.xml us-application-body.xsl application-body.dtd
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	isoamsa.ent
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	FIS920030415US1Drawing8.tif
us-declaration	FIS920030415US1Declaration1.tif
us-declaration	FIS920030415US1Declaration2.tif
<b>Comments</b>	
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